IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of: LYSANDER LIM ET AL.

Filed:

**FEBRUARY 13, 2002** 

For:

APPARATUS AND METHODS FOR GENERATING RADIO

FREQUENCIES IN COMMUNICATION CIRCUITRY

RECEIVED

Serial No.:

10/075,098

DEC 2 6 2002

Group Art Unit:

2685

**Technology Center 2600** 

Examiner:

Atty Dkt:

SILA:075

**UNKNOWN** 

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

INFORMATION DISCLOSURE STATEMENT

**Assistant Commissioner for Patents** Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in

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accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:075.

Per 37 CFR 1.98(d), no copies of references A1-A48, B1-B6, C1-90 and C101 have been provided, as copies of these references have been previously submitted to the Office in one or more of co-pending U.S. Patent Application Serial Nos. 09/821,340 filed on March 29, 2001, which is entitled "Digital Interface In Radio-Frequency Apparatus And Associated Methods" and 09/821,342 filed on March 29, 2001, which is entitled "Partitioned Radio-Frequency Apparatus And Associated Methods" and which is relied upon by the present application for an earlier effective filing date under 35 U.S.C. Section 120.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,

Maximilian R. Peterson

Reg. No. 46,469

Attorney for Applicant

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**Enclosures** 

List of Patents and Publications for Applicant's

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**U.S. Patent Documents** 

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	A1	5,828,955	10/27/98	Lipowski et al.			8/30/95
	A2	6,035,186	3/7/00	Moore et al.			3/11/97
	A3	6,075,979	6/13/00	Holtvoeth et al.			3/5/97
	A4	5,764,171	6/9/98	Stikvoort			4/2/96
	A5	6,148,048	11/14/00	Kerth et al.			9/26/97
	A6	4,713,563	12/15/87	Marshall et al.			5/12/86
	A7	4,070,632	1/24/78	Tuttle			9/22/76
	A8	4,236,252	11/25/80	Kominami et al.			2/6/79
	A9	4,680,588	7/14/87	Cantwell			12/5/85
	A10	4,857,928	8/15/89	Gailus et al.			1/28/88
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	A16	5,235,410	8/10/93	Hurley			7/10/91
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	A19	5,345,406	9/6/94	Williams			8/25/92
	A20	5,430,890	7/4/95	Vogt et al.			11/20/92
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	A25	5,712,628	1/27/98	Phillips et al.			8/31/95
	A26	5,742,189	4/21/98	Yoshida et al.			9/14/95
	A27	5,862,465	1/19/99	Ou			12/30/96
	A28	5,973,601	10/26/99	Campana			12/2/97
	A29	5,758,276	5/26/98	Shirakawa et al.			5/31/96
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	A31	4,623,926	11/18/86	Sakamoto			11/9/836
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/	A107	5,644,270	7/1/97	Moyer et al.			3/15/96
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	A111	5,844,868	12/1/98	Takahashi et al.			3/26/97
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	A120	5,661,269	8/26/97	Fukuzaki et al.			3/17/95
	A121	5,561,398	10/1/96	Rasmussen			5/16/95
	A122	5,619,148	4/8/97	Guo			10/10/95
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	A124	6,208,488	2/22/00	Landman et al.			10/30/97
	A125	6,130,577	10/10/00	Tamba et al.			6/11/96
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,	A128	4,255,714	3/10/81	Rosen			2/21/79
	A129	5,006,819	4/9/91	Buchan et al.			5/21/90
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	B2	GB2233518A	1/9/91	Dedic			
-	В3	0643477A2	3/15/95	Hulkko et al.			
	B4	WO 00/11794	3/2/00	Moore et al.			
	B5	WO 00/01074	1/6/00	Van Der Zwan et al.			
	В6	WO 99/22456	5/6/99	Grenabo			10/27/98
	В7	JP359127408 A	7/23/84	Shibata et al.			1/11/83
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	C1	Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.			
	C2	Stephen Jantzi et al, "A Complex Bandpass ΔΣ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.			

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	C4	Data Sheet, CX74017, "RF Transceiver for Single, Dual, or Tri-Band GSM/GPRS Applications," Conexant, January 2, 2001, pp. 1-16.
	C5	Jacques C. Rudell et al, "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2071-2088.
	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.
	C7	Jacques C. Rudell et al., "Recent Developments In High Integration Multi-Standard CMOS Transceiver for Personal Communication Systems," invited paper at the 1998 International Symposium on Low Power Electronics, Monterey, California, 6 pgs.
	C8	Asad Abidi, "CMOS Wireless Transceivers: The New Wave," IEEE Communications Magazine, August 1999, pp. 119-124.
	C9	Data Sheet, UAA3535HL, "Low Power GSM/DCS/PCS Multi-band Transceiver," Philips Semiconductors, February 17, 2000, pp. 1-24.
	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass ΔΣ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.
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	C12	Qiuting Huang, "CMOS RF Design-The Low Power Dimension," IEEE 2000 Custom Integrated Circuits Conference, pp. 161-166.
	C13	Paolo Orsatti et al., "A 20-mA-Receive, 55-mA-Transmit, Single-Chip GSM Transceiver in 0.25-µm CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp. 1869-1880.

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	C15	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pp. 428-435.
	C16	Farbod Behbahani et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection," IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pp. 873-887.
	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topolgy," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
	C18	Analog Devices, Single-Chip Direct-Conversion GSM/GPRS/EDGE RFIC, Othello One, <a href="https://www.analog.com">www.analog.com</a> , 2 pgs.
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	C20	Analog Devices, GSM 3 V Transceiver IF Subsystem, AD6432, www.analog.com, pp. 1-20.
	C21	Hitachi, "RF Transceiver IC For GSM And PCN Dual Band Cellular Systems," HD155121F, ADE-207-265(Z), 1st Edition, November 1998, pp. 1-56.
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